

iBERT 10000 40Gbps AOC Tester



Description

iBERT 10000 - 40Gbps AOC Tester is a 40Gbps Bit Error Rate Tester (BERT) with modularized interface which includes QSFP+ cage, SFP+ cage, or SMA type connector. There are 8 transmitters and 8 receivers to accomplish full duplex 40Gbps bidirectional data link. Each transmitter and receiver provide 10Gbps data link. Compliant with 40G Ethernet standards, the QSFP+ port follows QSFP+ MSA. User interface can individually monitor bit error rate, error count and timer via USB cable with PC. The serial ID and Digital Diagnostics Monitor for QSFP+ or SFP+ transceiver can also be monitored in the user interface.

Features

- 19" rackmount size
- No need for warming up
- Standard USB for connecting with PC. OS needs Windows XP or above and Microsoft .NET Framework 4.
- QSFP+, SFP+, or SMA interface module
- Friendly Graphic User Interface (GUI)

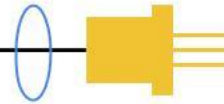
Applications

- 40 Gbps AOC/DAC Qualification
- 40 Gbps QSFP+ to 4 x SFP+ AOC/DAC Qualification
- 40 Gbps QSFP+ Qualification

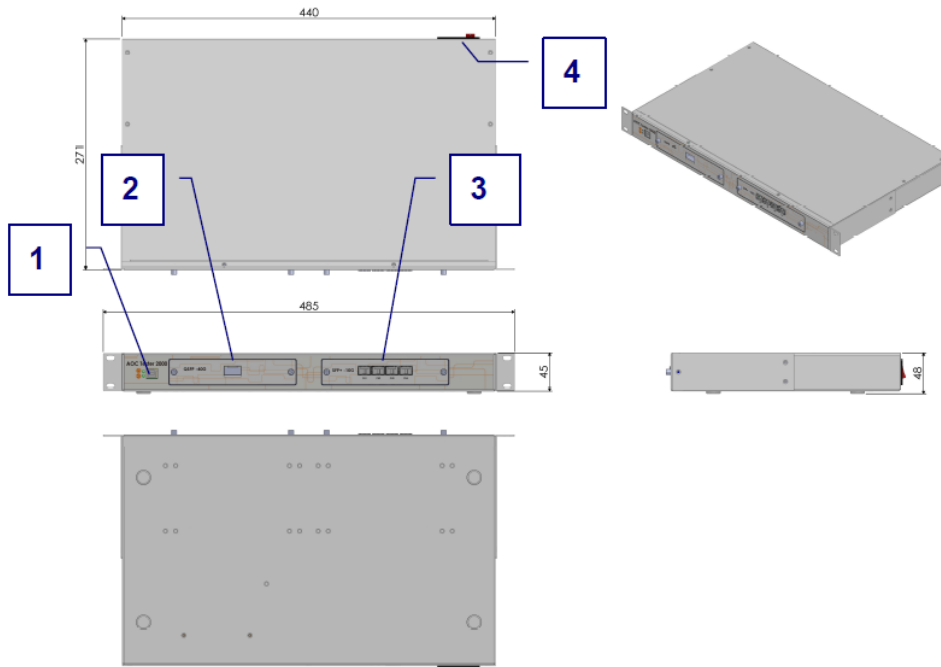
Specifications

Main Frame	
Slots	2 universal slots for installing modules
Operating Temperature	0°C ~ 50°C
Storage Temperature	-10°C ~ 70°C
Data Rate	10.3125Gbps x 4
Power Supply	100-240VAC
Weight	2.0kg
Dimensions (WxDxH)	485mm x 271mm x 48mm

Slot Boards	
QSFP+ Module	1 standard QSFP+
SFP+ Module	4 standard SFP+
TX SMA Module	4 pairs TX electrical
RX SMA Module	4 pairs RX electrical



Elements and Operating Instructions



1. USB (B Type): USB port. Connect to host via a standard USB cable
2. Slot L: Slot for installing QSFP+/SFP+/SMA interface module
3. Slot R: Slot for installing QSFP+/SFP+/SMA interface module
4. Power Input: AC power input

Graphical User Interface (GUI)

BERT Page:

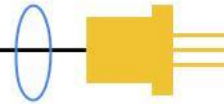
1. COM PORT: The COM port number of the Tester. The number is defined in host hardware manager. This COM port number **MUST** be selected correctly.
2. RATE CONTROL: Transmitter/Receiver data rate standards selection.
3. LINK: Link with the tester. It shows PASS for correct setting of hardware connection and shows FAIL otherwise.
4. EXIT: Exit this program. It changes to START after click.
5. Disconnect: Disconnect and release the COM port link.
6. START: Start for the bit error rate test. It changes to STOP after click.
7. STOP: Stop for the bit error rate test.
8. RESET: Reset the bit error counts and rates.
9. BERT Mode: Free run is for untimed testing; timer run is for limited time testing.

Digital Diagnostics Page:

This page shows the Digital Diagnostics data from transceivers. It complies with SFF-8472 for SFP+ and SFF-8436 for QSFP. Not support for SMA modules.

Serial ID Page:

This page shows the Serial ID from transceivers. It complies with SFF-8472 for SFP+ and SFF-8436 for QSFP. Not support for SMA modules.



Maintenance

Like any other type of electronic equipment, this QSFP checker should be kept away from water, high humidity, dust, electricity, and environments of extreme temperatures. Do not drop this tool on any hard surface. Internal modification of any of the SFP checker components can cause a malfunction and will invalidate the manufacturer's warranty.

Warranty

The manufacturer warrants this product to be free of defects in workmanship and materials for a period of 10 months after purchase. This warranty is solely limited to the repair or replacement of the original parts. All other costs are the sole responsibility of the owner. This warranty does not cover any defects, damage, or deterioration due to misuse, alteration, or negligence.

Ordering Information

Part Number	Description
iBERT 10000 (S201313009999)	iBERT 10000 40G AOC Checker Main Frame
S201313-3	1-QSFP+ slot board
S201313-2	4-SFP+ slot board
S201313-4T	4-TX SMA slot board
S201313-4R	4-RX SMA slot board

Note: Specifications are subject to change without notice.