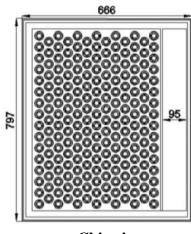


Model No. VCC-94A1WH

Features:

- 940nm VCSEL chip
- Typical 1W peak pulse output at 1.2A
- High PCE (Power Conversion Efficiency) 42%
- -20 to 85 °C operating temperature
- Chip size: 666 x 797± 15 μm
- Electrode Side:
 Gold alloy on both anode P (emission side) and cathode N (backside)



Chip size

Tel: (909)718-0999

Fax: (909)718-0998

Applications:

- Sensing light source
- Optical encoders
- Photoelectric sensors
- 3D sensing
- 3D imaging including Time of Flight, Structure light, Iris/ Facial recognition etc.

Absolute Maximum Ratings (TA = 25°C unless otherwise noted)

Parameter	Symbol	Rating
Storage Temperature	Tstg	-40 to 85 °C
Operating Temperature	Тор	-20 to 85 °C
Continuous Forward Current	lf	1.3A
Maximum package SMT solder reflow Temperature	-	260°C, 10 seconds

Note: The maximum CW laser current in the Absolute Maximum Ratings is valid for the operating temperature noted at the table above. Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device.

Electro-Optics Characteristics (Ta=25°C unless otherwise noted)

Parameters	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Threshold Current	Ith	-	180	-	mA	
Slope Efficiency	η	0.9	1.0	-	W/A	If = 1.2A
Optical Output Power	Po		1000	-	mW	If = 1.2A
Center Wavelength	λς	930	940	950	nm	If = 1.2A
Beam Divergence	Θ		25		degree	Full Width 1/e ²
Operating Voltage	Vf	1.7	2.0	2.3	V	If = 1.2A
Power Conversion Efficiency	PCE	-	42	-	%	I _f = 1.2A
Wavelength Shift	Δλ/ΔΤ	-	0.07	-	nm/°C	If = 1.2A

Note 1: Forward Voltage (V_f) measurement allowance is ±0.1V.

Note 2: Center Wavelength (λ .) measurement allowance is ± 1.5 nm.

Note 3: Others measurement allowance is ±10%.

Note 4: Test DUTs are mounted on star board and measured with operating bias current @ 1.2A, Duty Cycle:10%.

Note: The specifications are subject to change without notice.

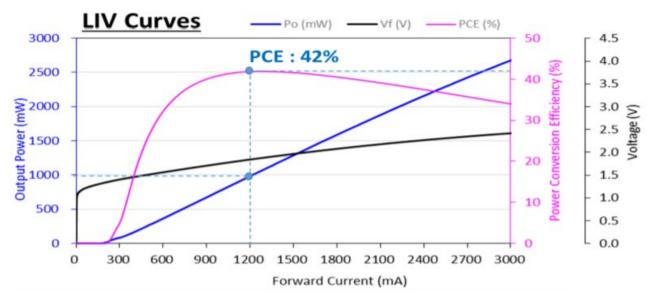
19608 Camino De Rosa, Walnut, CA 91789, USA e-mail: info@lasermate.com/URL: http://www.lasermate.com/

Typical Performance Graph:

Beam Divergence

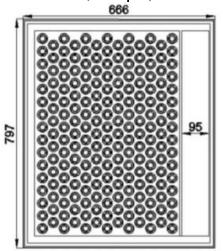
Full Width 1/e² 25 degree

LIV Graph at 25 °C



Note: Curves measurement at $0 \sim 3A$ current sweep with 10% duty cycle.

Dimensions (unit: µm):



Specification	Min	Тур	Max
Chip width	651	666	681
Chip length	782	797	812
Chip thickness	105	120	135
Bond pad width	-	95	-

Unit: µm

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Note: The specifications are subject to change without notice.

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